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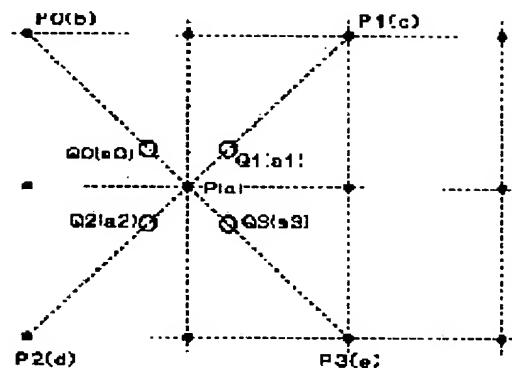
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## (54) IMAGE PROCESSING CIRCUIT

## (57)Abstract:

PROBLEM TO BE SOLVED: To provide an image processing circuit performing processing at high speed and increasing horizontal/vertical resolution with simple circuit constitution.

SOLUTION: By using a pixel of interest P and two adjacent pixels P0, P3 arranged on the nearest position in the oblique direction with the pixel of interest P inbetween, interpolation pixels Q0, Q3 on the diagonal line linking these pixels P0, P3 are obtained. When the pixel value of the pixel of interest P is denoted by (a), and respective pixel values of the pixels P0, P3 are denoted by b, e, the pixel value a0 of the interpolation pixel Q0 existing at the position of 1/4 of a distance from the pixel of interest P to the pixel P0, is obtained as  $(8a+b-e)/8$ , and the pixel value a3 of the interpolation pixel Q3 existing at the position of 1/4 of the distance from the pixel of interest P to the pixel P3 is obtained as  $(8a-b+e)/8$ . In such a manner, by using the interpolation pixel instead of the pixel of an original image, an image display that has the number of horizontal lines is doubled and has number of pixels which is twice that of pixels constituting respective horizontal lines is performed.



## LEGAL STATUS

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